


Arun A V		
PROFILE	Asst.Professor (ElectronicsEngg.) Model Engineering College,Ernakulam. Email : arunav.aav@gmail.com Ph: +91 9544640794	
EXPERIENCE	7 YEARS IN TEACHING	
EDUCATION		
M.Tech –VLSI Design	Anna University	
B Tech – Applied Electronics & Instrumentation	Marian Engg College, University of Kerala	
AREAS OF INTEREST		
Semiconductor device modelling, CMOS VLSI Design, Digital System Design		
MEMBERSHIP IN PROFESSIONAL BODIES		
Member of Institute of Electrical & Electronics Engineers (IEEE): No. 93068329		

Publications

SI No	Paper Title	Name of Conference	Venue	Year
1	Gate Tunable Thermionic Tunnel FET with n+ Pocket	INDICON 2018	Amrita VishwaVidhyapeetham, Coimbatore	2018
2	A novel approach for improving the performance enhancement of InAs/Si tunnel FET	National Conference on Recent innovations in information and communication technologies	Ahalia school of engineering & technology, Palakkad	2017
3	Detection of arrhythmia by ECG signal and its implementation	International summit on electrical,electronics, computer science and communication engineering	Kochi	2017
4	Background detection in military reconnaissance using modified intersection solving method	International Conference on Computer Science (ICCS)	Easa college of engineering, Coimbatore	2014
5	High performance generalized punctured convolutional codes	International Conference on Electrical Engineering and Computer Science (ICEECS)	Inter science research network, Trivandrum	2012

COURSES HANDLED

Sl.No	Course Name	UG/PG
1	CMOS Digital Design	PG (VLSI & Embedded Systems)
2	System on Chip Design	PG (VLSI & Embedded Systems)
3	Analog Integrated Circuit Design	PG (VLSI & Embedded Systems)
4	VLSI	B Tech Electronics & Communication Engg. (KTU)
5	Digital System Design	B Tech Electronics & Communication Engg. (KTU)
6	Analog Integrated Circuits	B Tech Electronics & Communication Engg. (KTU)
7	Digital Electronics	B Tech Electronics & Communication Engg. (University of Calicut)