

Notice

The lab examination for the following students will be conducted as per the details shown below:

Date of Examination: 9.2.2017

Time: 1.30-4.30 PM

Subject: CS013L2 ELECTRONICS DEVICES & CIRCUIT LAB

REGISTER NUMBER	NAME
12130866	AKASH N MADHAVAN
12140902	REHANA A JALEEL
12150809	ALBIN ANTONY
12150846	NAEEMUDHIN K PUTHIYANNAL
12150859	SANJAY S PRABHU
12150869	VISHNU V S
12120864	AJITH VARGHESE

The examiner is Dr Binesh T. The students should report in the Electronic Circuits Lab by 1.20 PM on 9.2.2017 with hall ticket and certified Lab records.

Dr Vinu Thomas
Professor & Head,
Department of Electronics & Communication.

FIFTH SEMESTER SUPPLEMENTARY LAB EXAM

The Supplementary Exam for CS 15L2 **computer graphics lab** is scheduled as follows

Date : 09/02/2017

Time : 1:30 PM to 4:30 PM

REGISTER NUMBER	NAME
12120864	AJITH VARGHESE

HOD (CS)

THIRD SEMESTER SUPPLEMENTARY LAB EXAM

The Supplementary Exam for **CS 13L1 Object Oriented Programming Lab** is scheduled as follows

Date : 10/02/2017

Time :9.30A.M TO 12.30P.M

REGISTER NUMBER	NAME
12130866	AKASH N MADHAVAN
12130897	MUHAMMED IRSHAD R H
12140702	RANI LAKSHMI M R
12140902	REHANA A JALEEL
12150846	NAEEMUDHIN K PUTHIYANNAL
12120864	AJITH VARGHESE

HOD (CS)

CS15L1/507 Microprocessor Lab (2006&2012 scheme of CS)

Date of examination : 10/02/2017 9:30 AM – 12:30 PM

REGISTER NUMBER	NAME
12110894	S.AKSHAY (507)
1212874	JAWAHAR (507)
12120864	AJITH VARGHESE(CS15L1)
12130897	MOHAMMED IRSHAD R H(CS15L1)

HOD CS